

U.S. PRO 10/033394

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10033394	FILING DATE 12/29/2001	CLASS 438	SUBCLASS L63	GAU 2912	EXAMINER
<b>**APPLICANTS:</b> Lee Seong-jae Cho Won-ju Park Kyoung-wan					
<b>**CONTINUING DATA VERIFIED:</b>					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> REPUBLIC OF KOREA 01-66742 10/29/2001					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiner's initials		ATTORNEY DOCKET NO. 2013p006			
TITLE : Method of fabricating integrated circuit having shallow junction					

<b>NOTICE OF ALLOWANCE MAILED</b>		Assistant Examiner   Primary Examiner		<b>CLAIMS ALLOWED</b>	
ISSUE FEE Amount Due      Date Paid				Total Claims	Print Claim for O.G.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		DRAWING Sheets Drawn      Figs. Drawn      Print Figs.		Application Examiner	
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